

FIG. 1

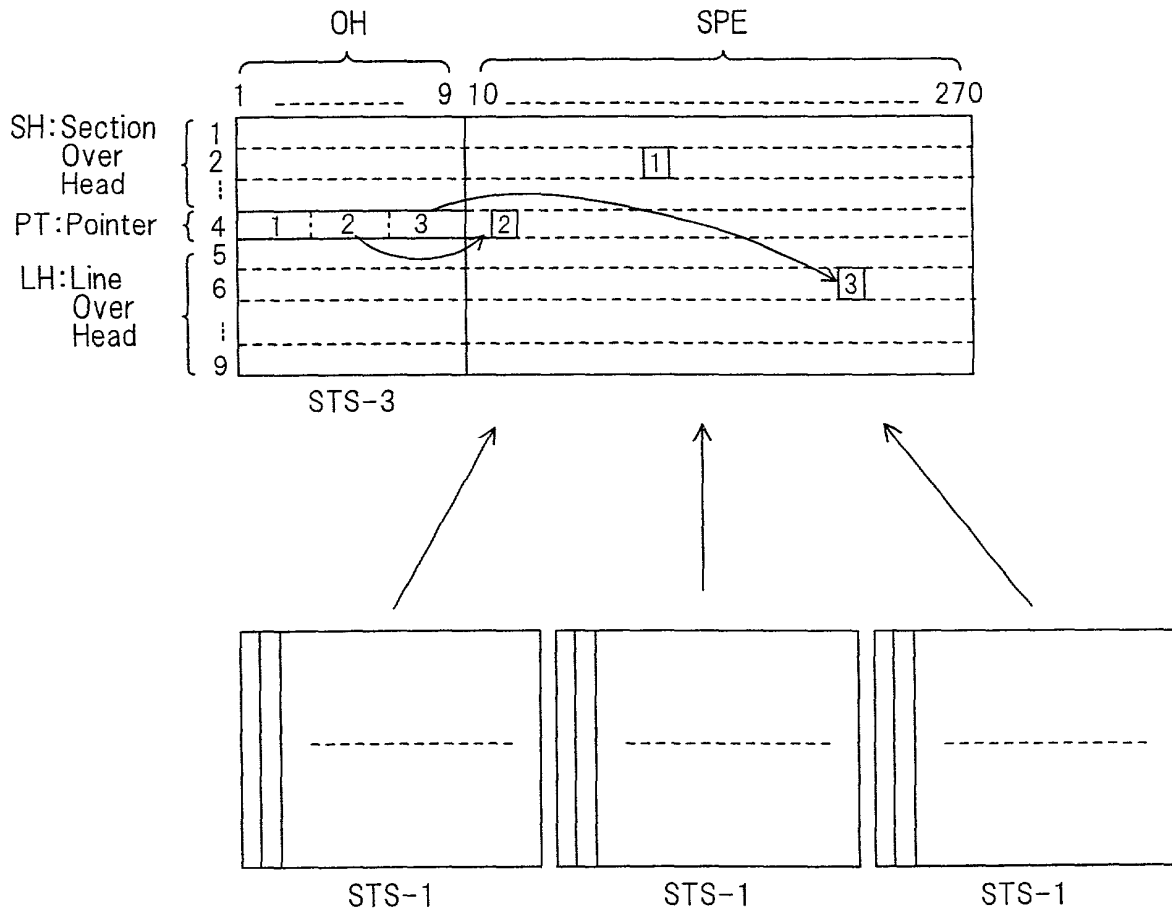


FIG. 2

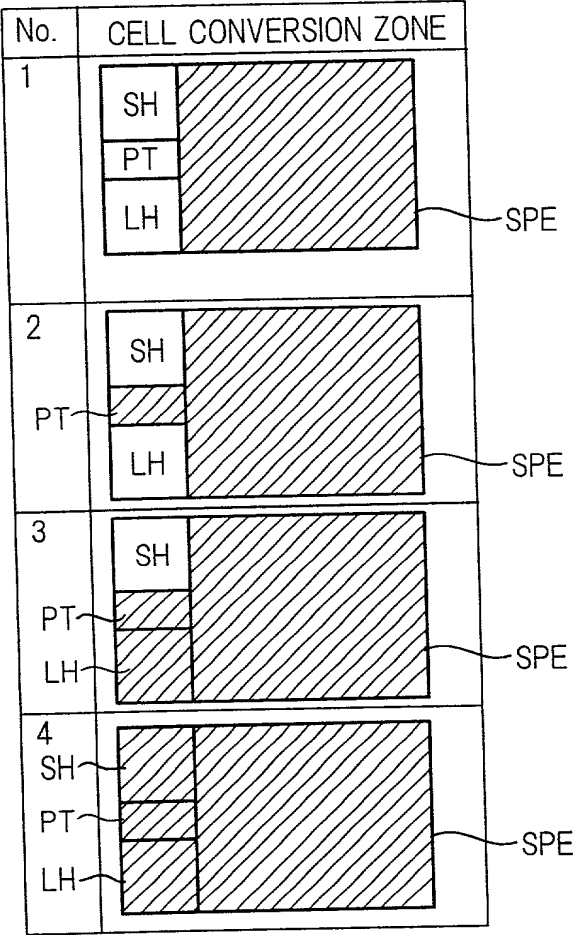


FIG. 3

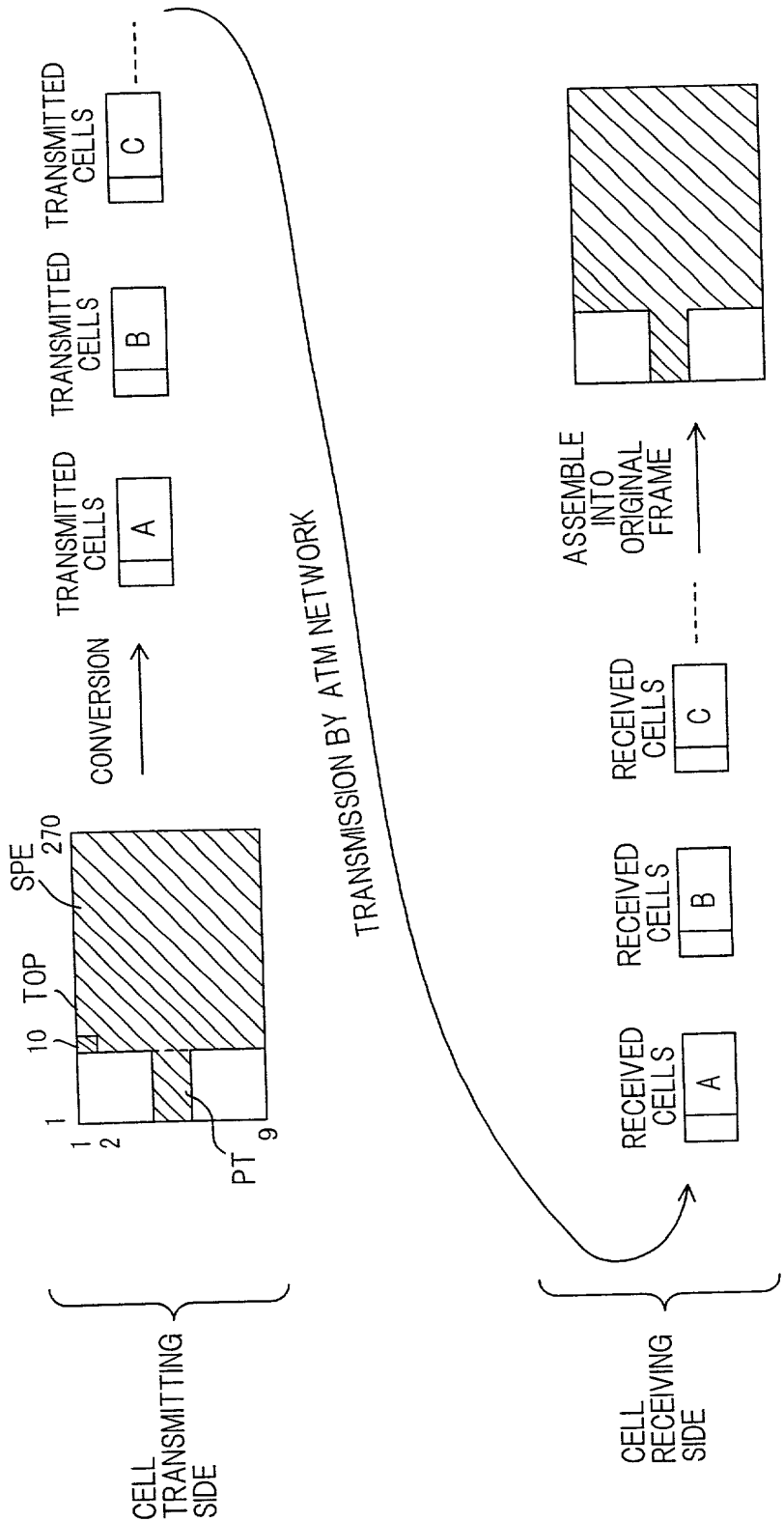
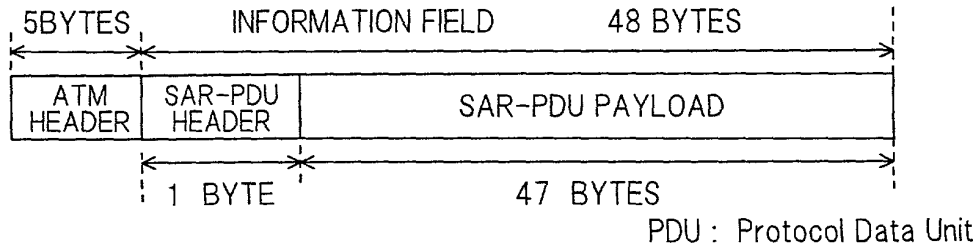
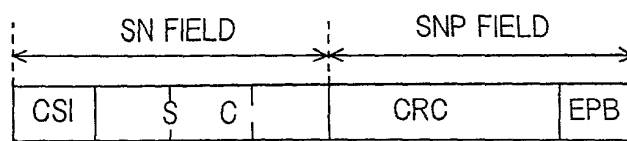


FIG. 4**FIG. 5**

CSI : Convergence Sublayer Identifier
 SC : Sequence Count
 CRC : Cyclic Redundancy Check
 EPB : Even Parity bit

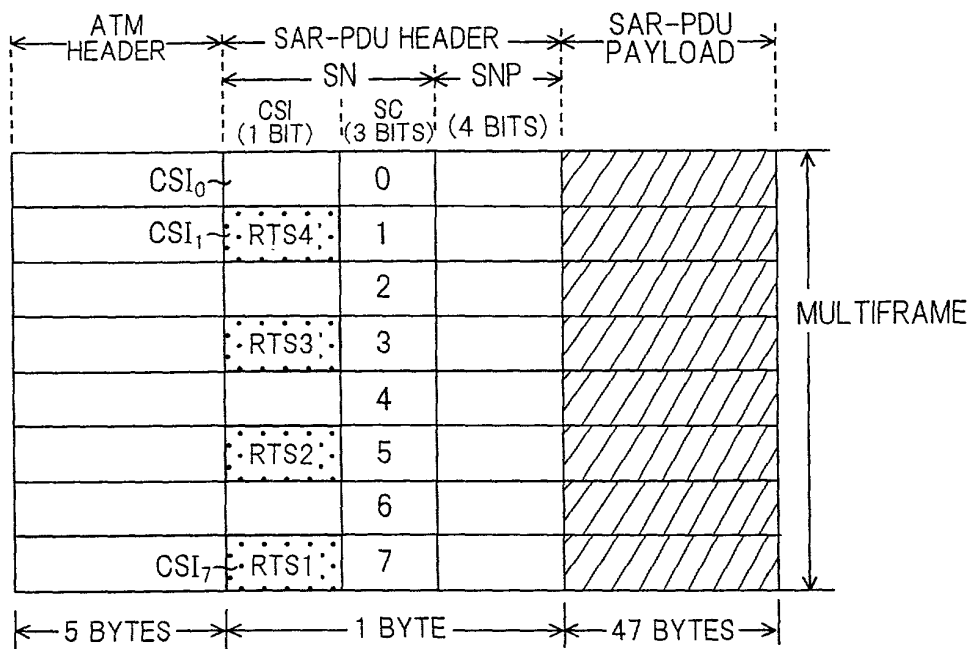
FIG. 6

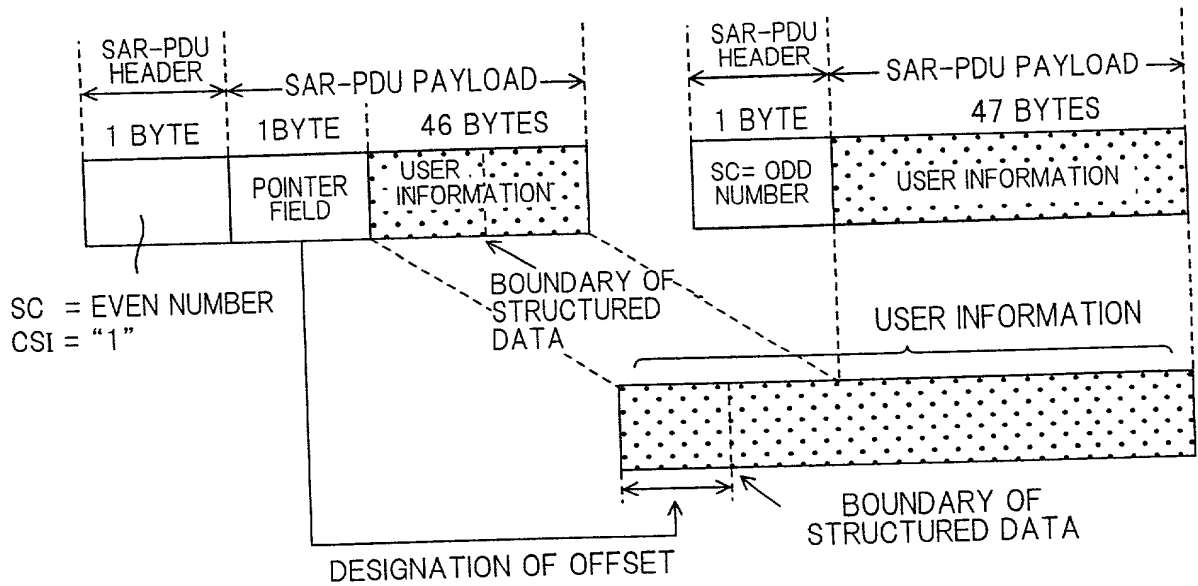
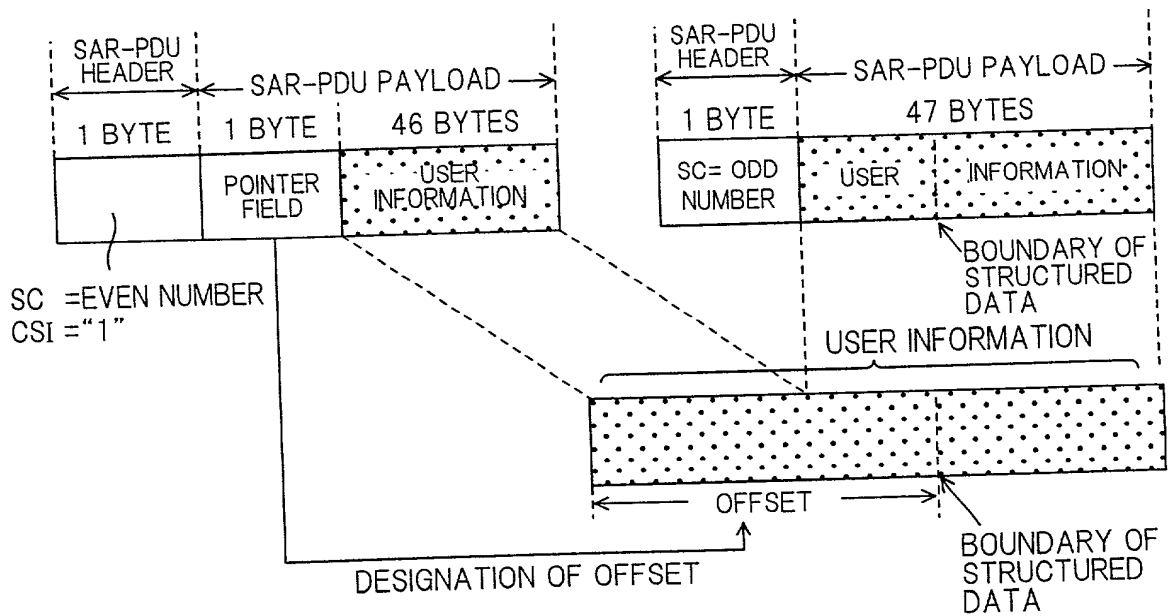
FIG. 7A*FIG. 7B*

FIG. 8

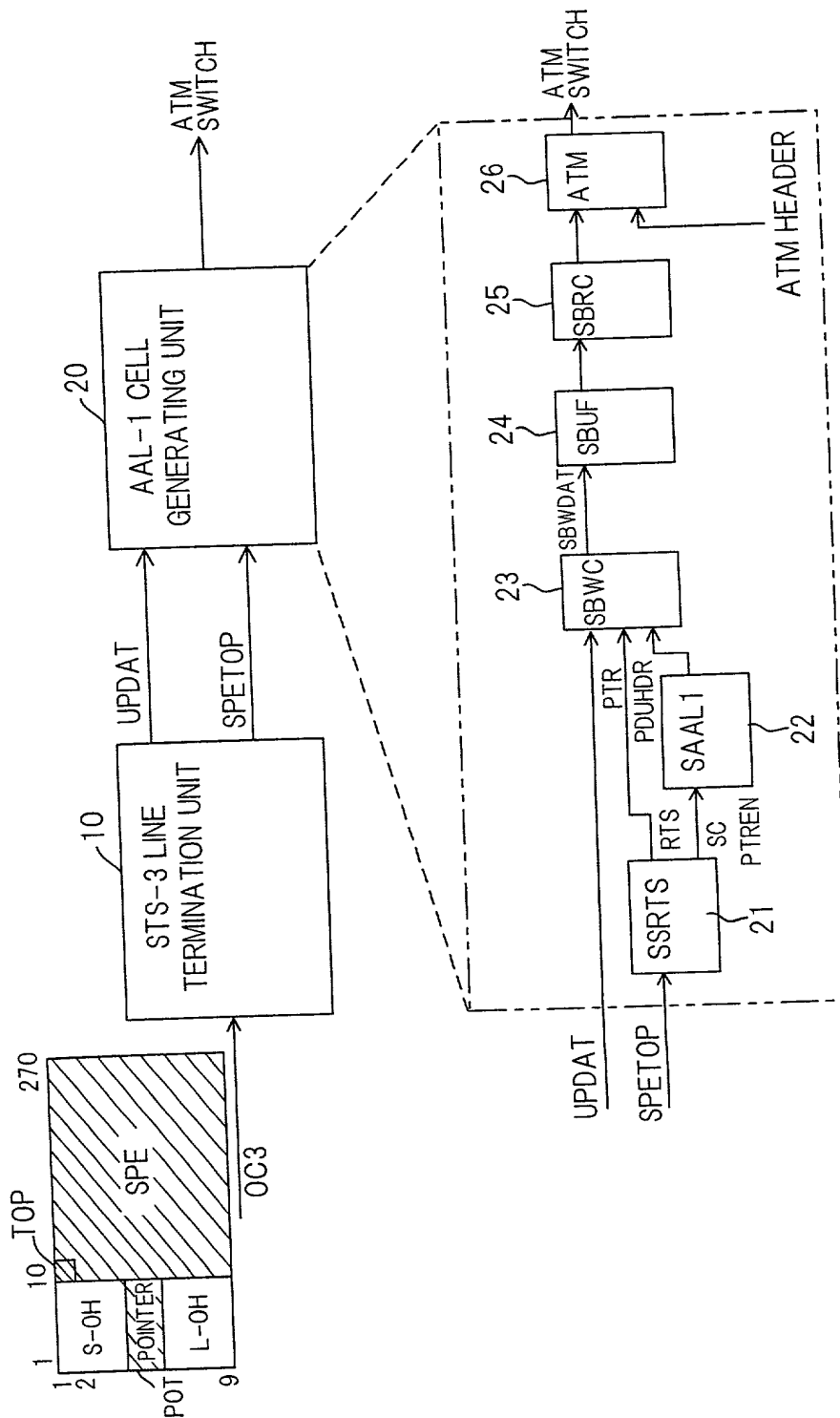


FIG. 9

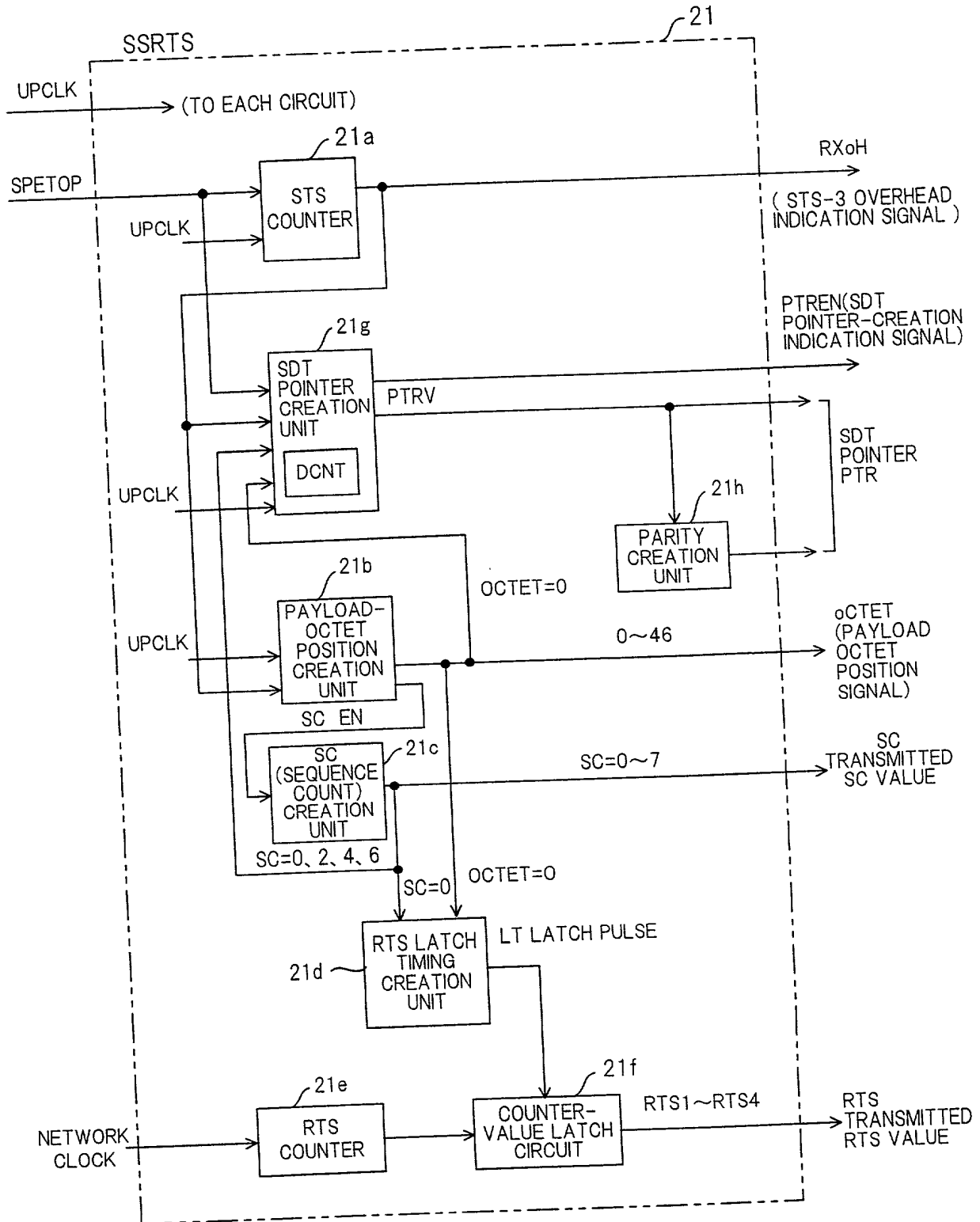


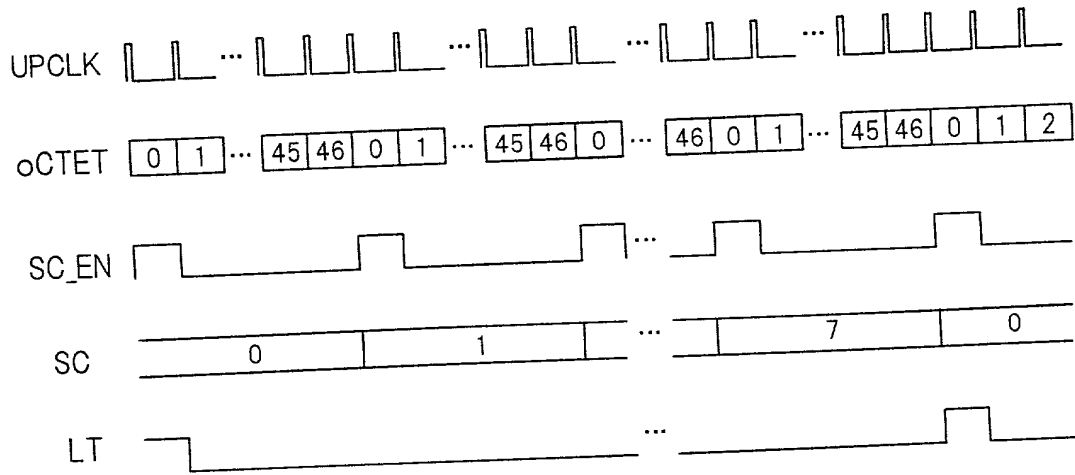
FIG. 10

FIG. 11

POSITION REPORTED BY TOP

A1#1	A1#2	A1#3	A2#1	A2#2	A2#3	J0	Z0#2	Z0#3	SPE 9	SPE 10	SPE 11		SPE 267	SPE 268	SPE 269
0	1	2	3	4	5	6	7	8							
B1	X	X	E1	X	X	F1	X	X	SPE 279	SPE 280	SPE 281		SPE 537	SPE 538	SPE 539
270	271	272	273	274	275	276	277	278							
D1	X	X	D2	X	X	D3	X	X	SPE 549	SPE 550	SPE 551		SPE 807	SPE 808	SPE 809
540	541	542	543	544	545	546	547	548							
H1#1	H1#2	H1#3	H2#1	H2#2	H2#3	H3#1	H3#2	H3#3	SPE 819	SPE 820	SPE 821		SPE 1077	SPE 1078	SPE 1079
810	811	812	813	814	815	816	817	818							
B2#1	B2#2	B2#3	K1	X	X	K2	X	X	SPE 1089	SPE 1090	SPE 1091		SPE 1347	SPE 1348	SPE 1349
1080	1081	1082	1083	1084	1085	1086	1087	1088							
D4	X	X	D5	X	X	D6	X	X	SPE 1359	SPE 1360	SPE 1361		SPE 1617	SPE 1618	SPE 1619
1350	1351	1352	1353	1354	1355	1356	1357	1358							
D7	X	X	D8	X	X	D9	X	X	SPE 1629	SPE 1630	SPE 1631		SPE 1887	SPE 1888	SPE 1889
1620	1621	1622	1623	1624	1625	1626	1627	1628							
D10	X	X	D11	X	X	D12	X	X	SPE 1899	SPE 1900	SPE 1901		SPE 2157	SPE 2158	SPE 2159
1890	1891	1892	1893	1894	1895	1896	1897	1898							
S1	Z1#2	Z1#3	M0	Z2#2	Z2#3	E2	X	X	SPE 2169	SPE 2170	SPE 2171		SPE 2427	SPE 2428	SPE 2429
2160	2161	2162	2163	2164	2165	2166	2167	2168							

* LOWER VALUE IS VALUE OF STS-CTR COUNT
 * BYTES WITHIN BOLD BORDER UNDERGO AAL-1 CONVERSION (RXoH = "0")

FIG. 12

POSITION REPORTED BY TOP



A1#1	A1#2	A1#3	A2#1	A2#2	A2#3	J0	Z0#2	Z0#3	SPE 2357	SPE 2356	SPE 2355	-----	SPE 2099	SPE 2098	SPE 2097
B1	X	X	E1	X	X	F1	X	X	SPE 2096	SPE 2095	SPE 2094	-----	SPE 1838	SPE 1837	SPE 1836
D1	X	X	D2	X	X	D3	X	X	SPE 1835	SPE 1834	SPE 1833	-----	SPE 1577	SPE 1576	SPE 1575
H1#1 1574	H1#2 1573	H1#3 1572	H2#1 1571	H2#2 1570	H2#3 1569	H3#1 1568	H3#2 1567	H3#3 1566	SPE 1565	SPE 1564	SPE 1563	-----	SPE 1307	SPE 1306	SPE 1305
B2#1	B2#2	B2#3	K1	X	X	K2	X	X	SPE 1304	SPE 1303	SPE 1302	-----	SPE 1046	SPE 1045	SPE 1044
D4	X	X	D5	X	X	D6	X	X	SPE 1043	SPE 1042	SPE 1041	-----	SPE 785	SPE 784	SPE 783
D7	X	X	D8	X	X	D9	X	X	SPE 782	SPE 781	SPE 780	-----	SPE 524	SPE 523	SPE 522
D10	X	X	D11	X	X	D12	X	X	SPE 521	SPE 520	SPE 519	-----	SPE 263	SPE 262	SPE 261
S1	Z1#2	Z1#3	M0	Z2#2	Z2#3	E2	X	X	SPE 260	SPE 259	SPE 258	-----	SPE 2	SPE 1	SPE 0

* LOWER VALUE IS VALUE OF DCNT COUNT

* BYTES WITHIN BOLD BORDER UNDERGO AAL-1 CONVERSION (RXoH = "0")

FIG. 13

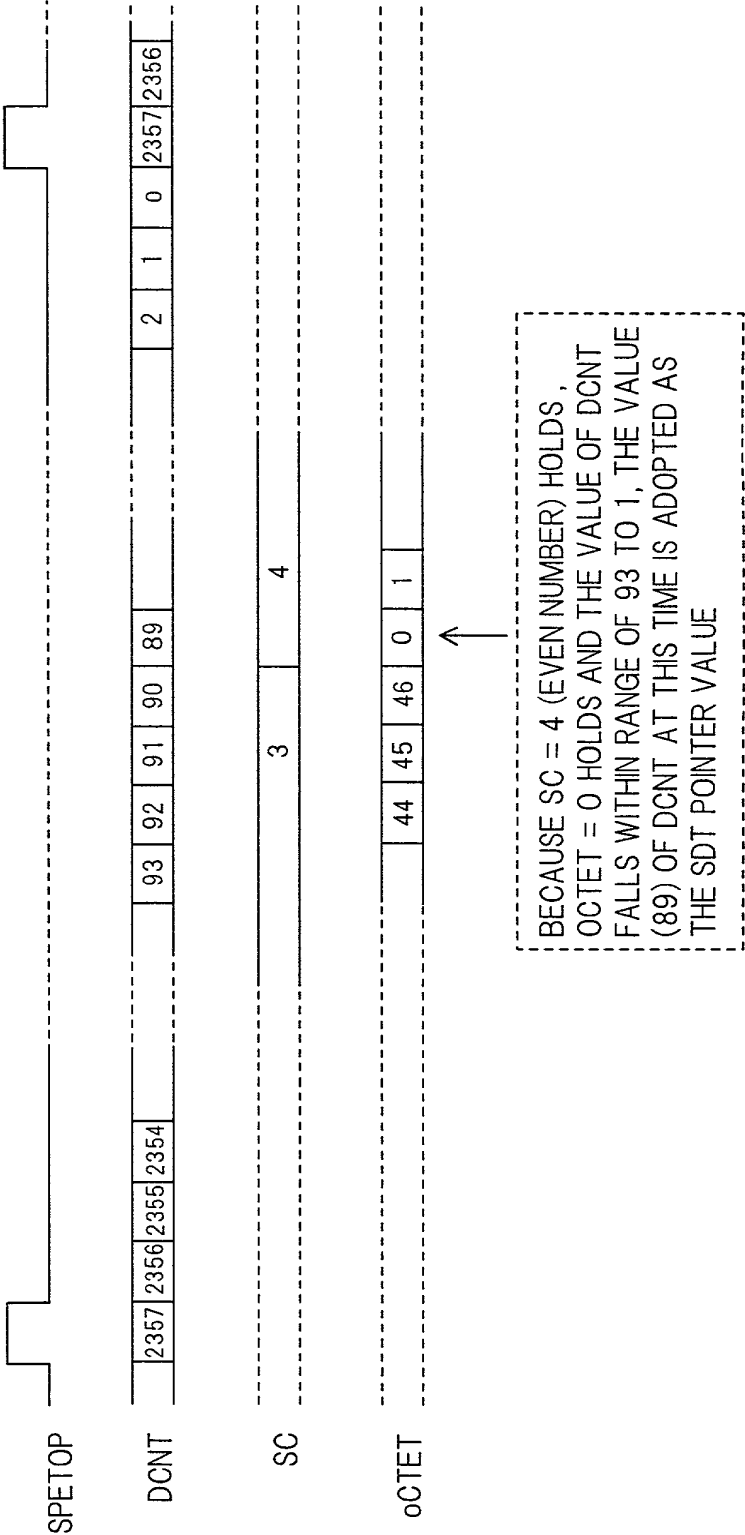


FIG. 14

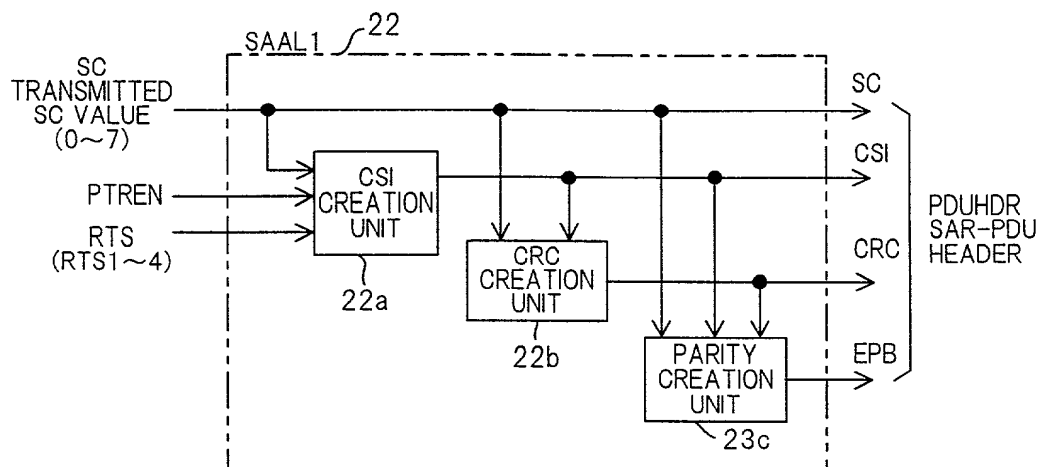


FIG. 15

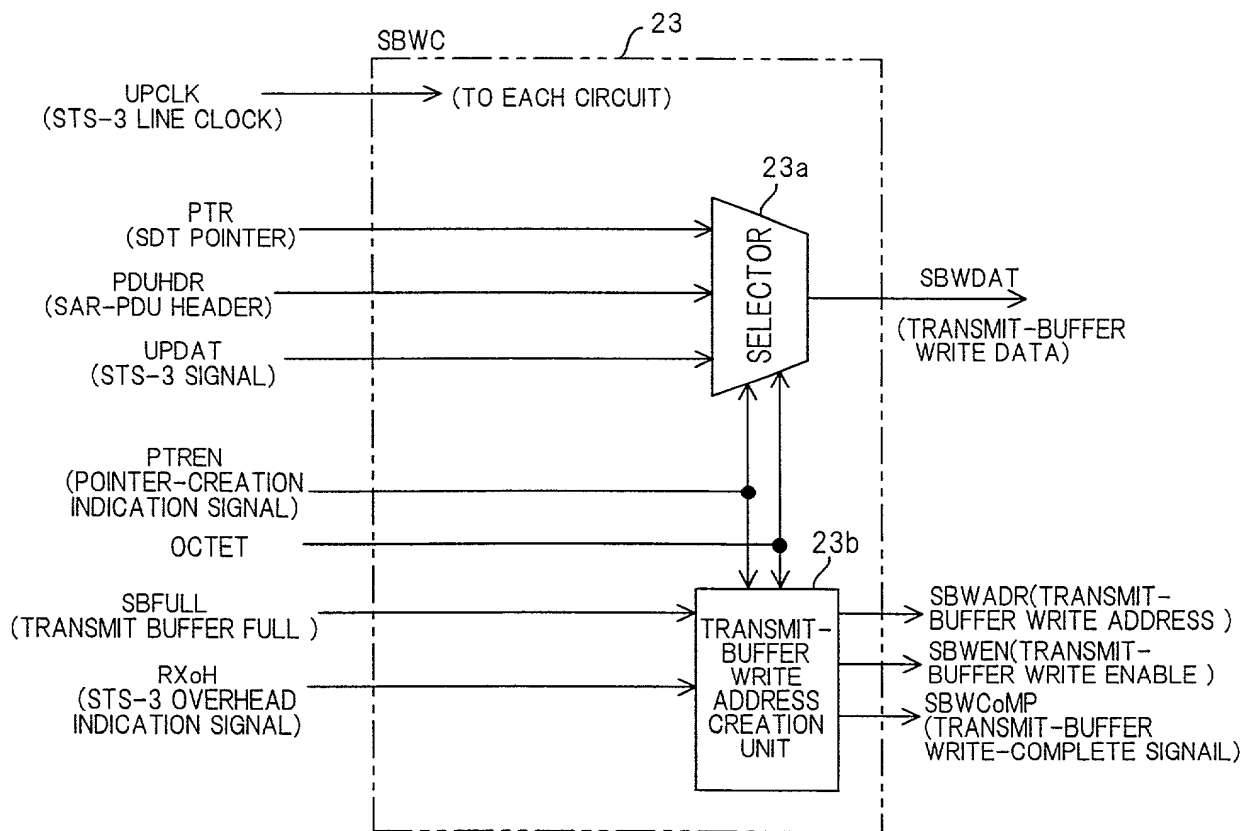


FIG. 16

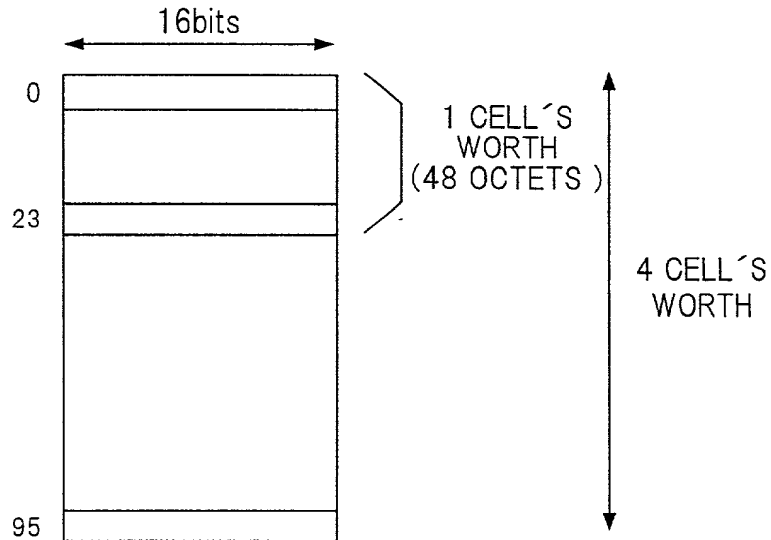


FIG. 17A

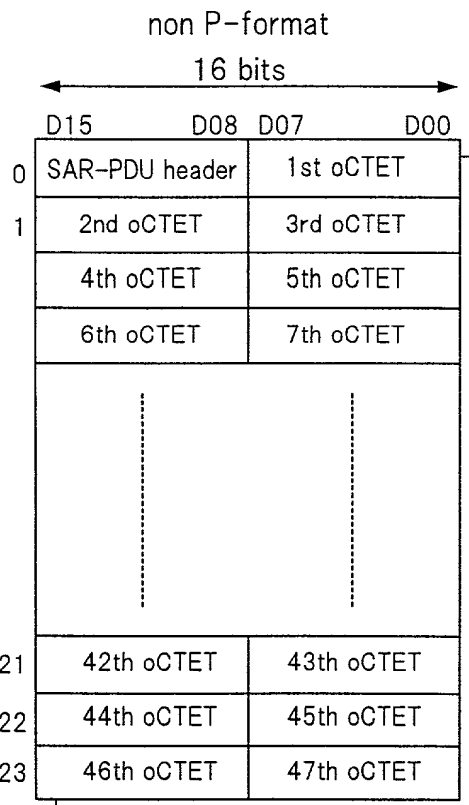


FIG. 17B

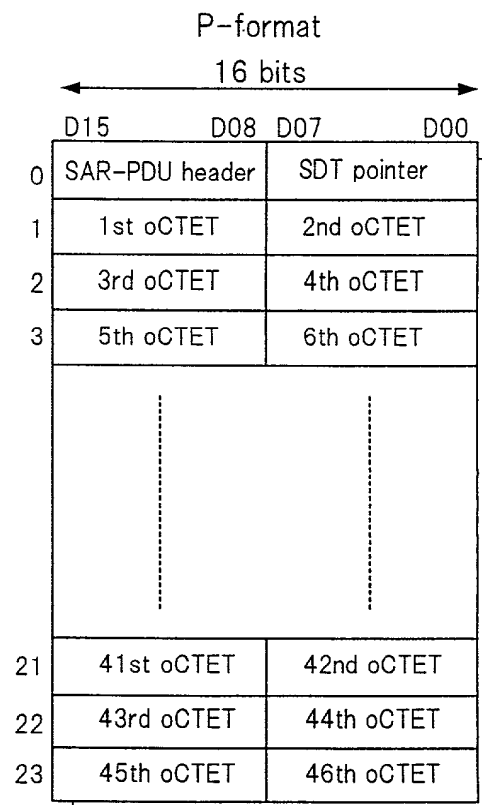


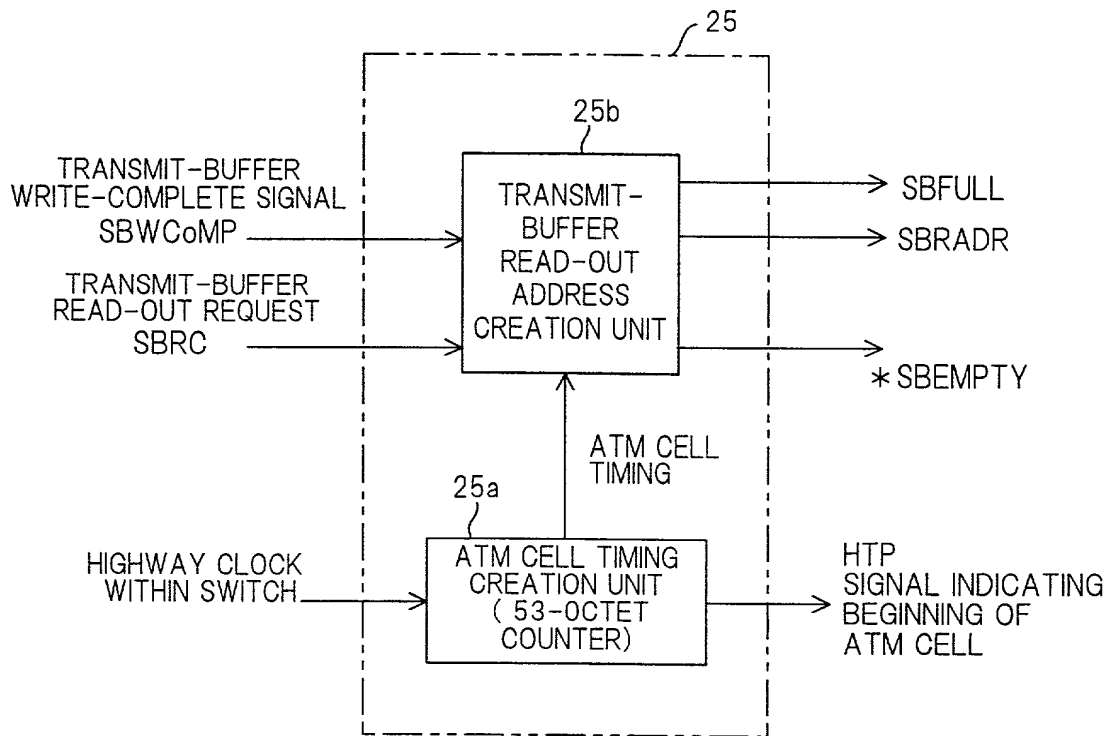
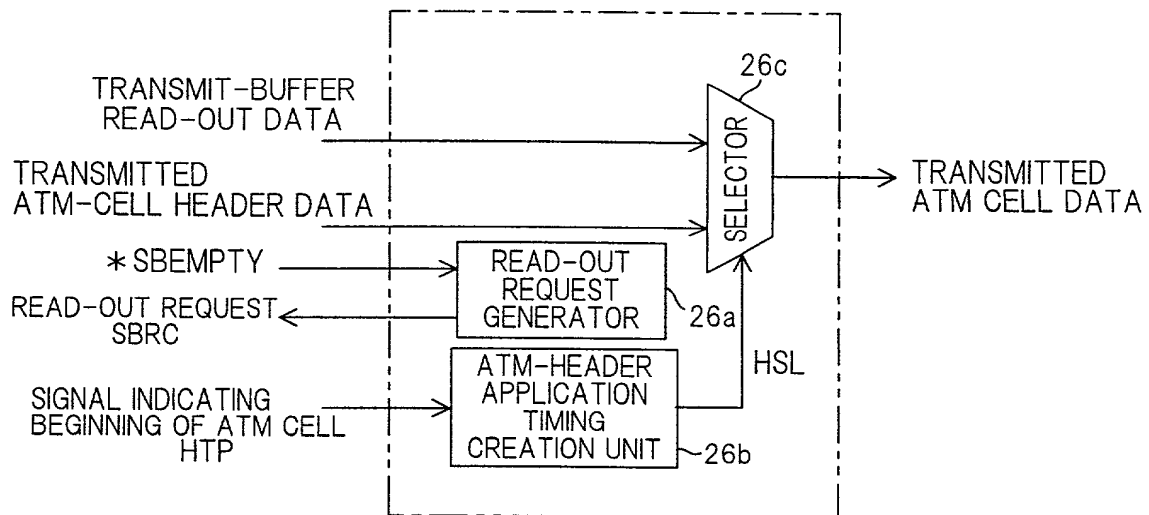
FIG. 18**FIG. 19**

FIG. 20

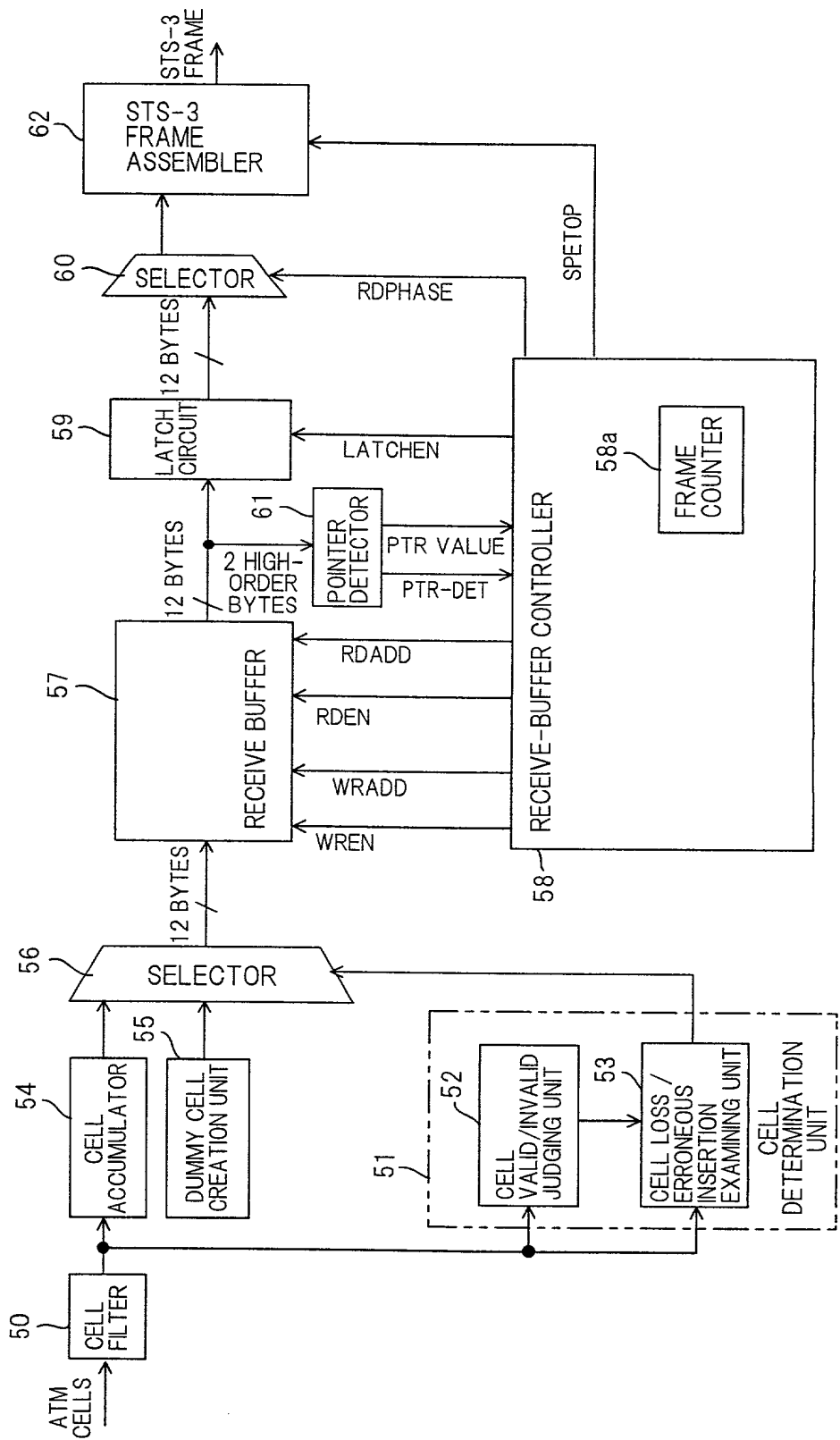


FIG. 21A

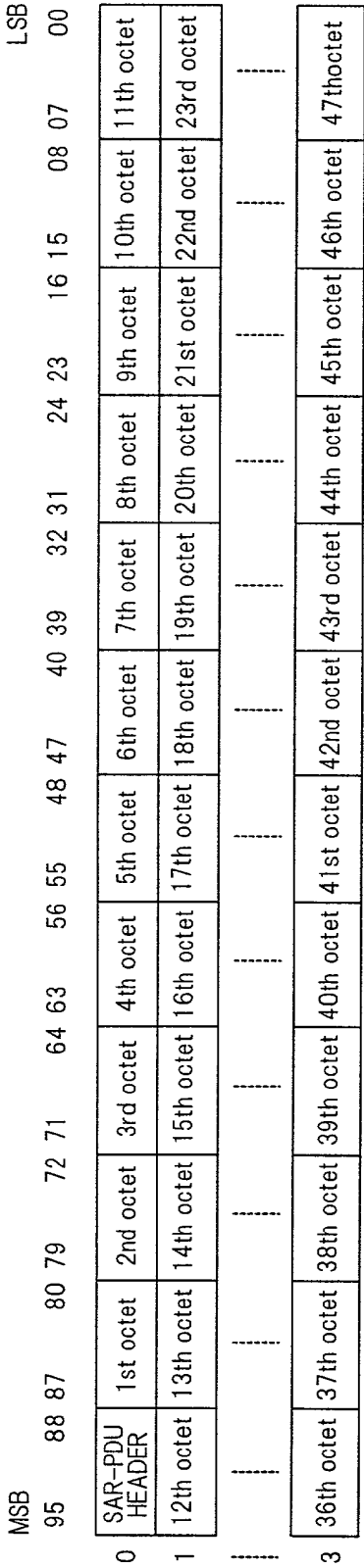


FIG. 21B

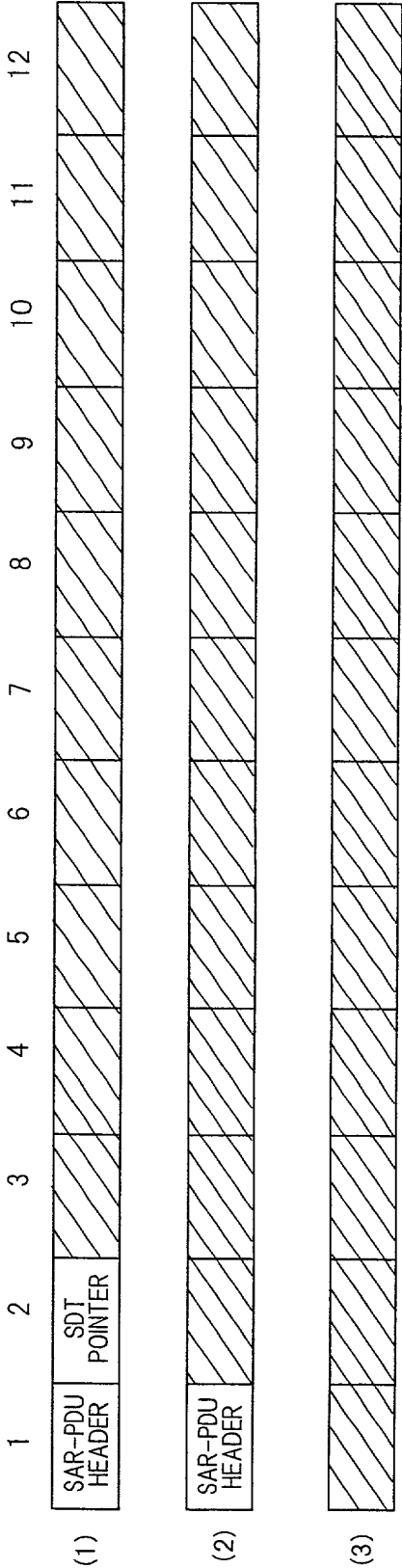


FIG. 22

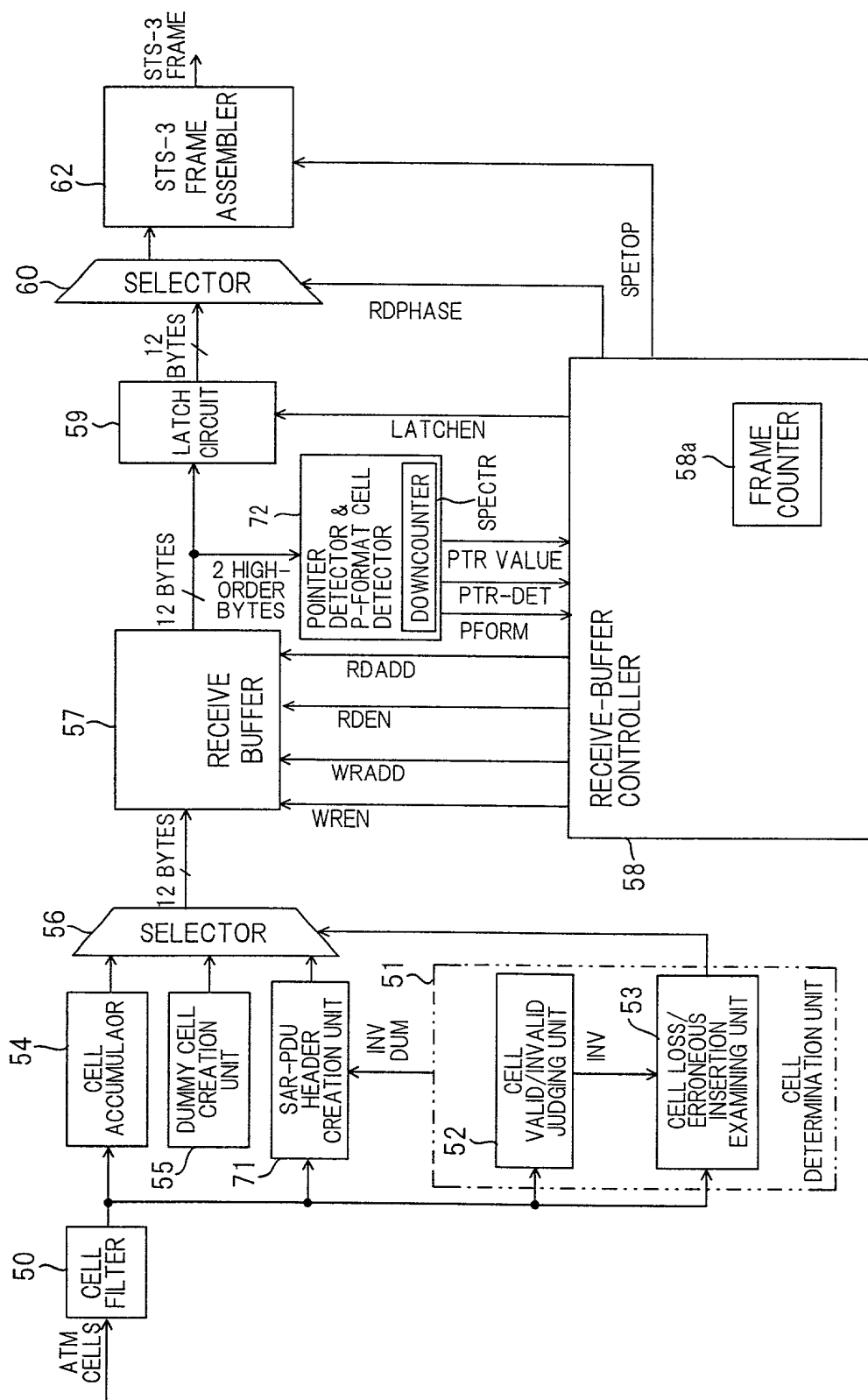


FIG. 23

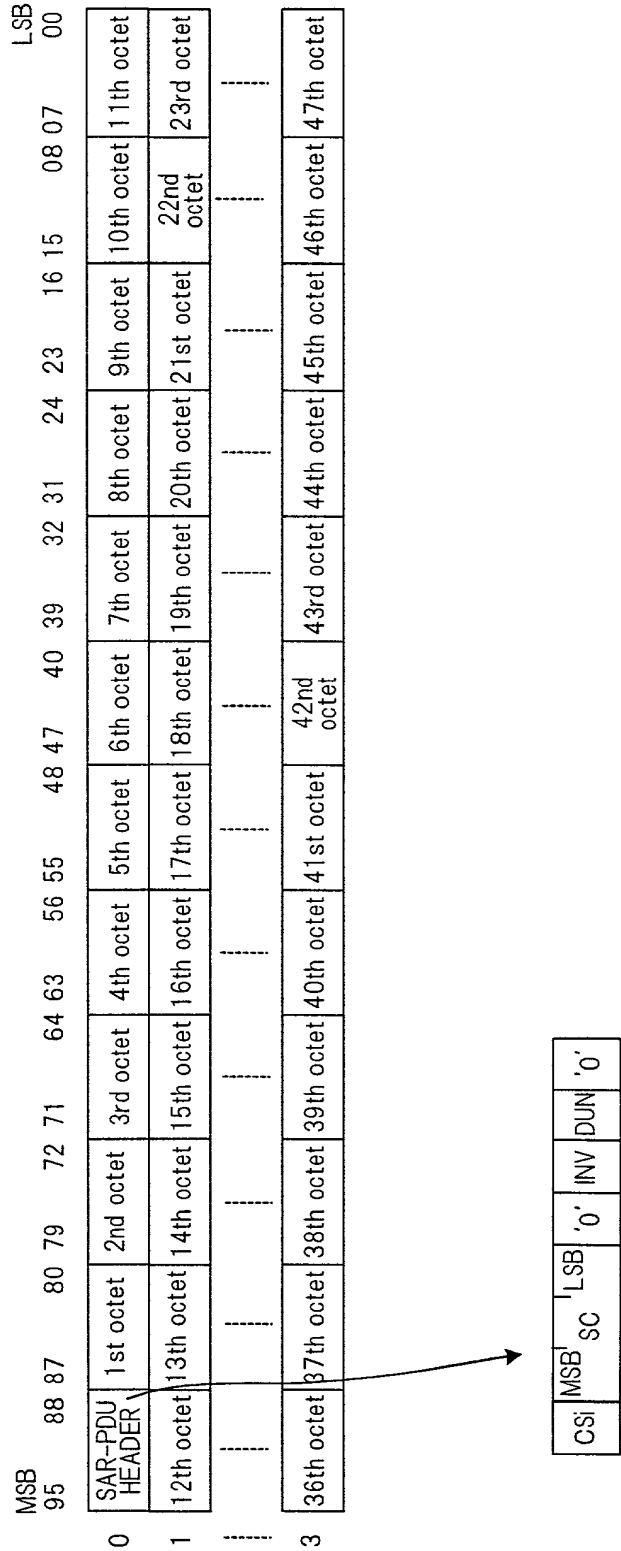


FIG. 24

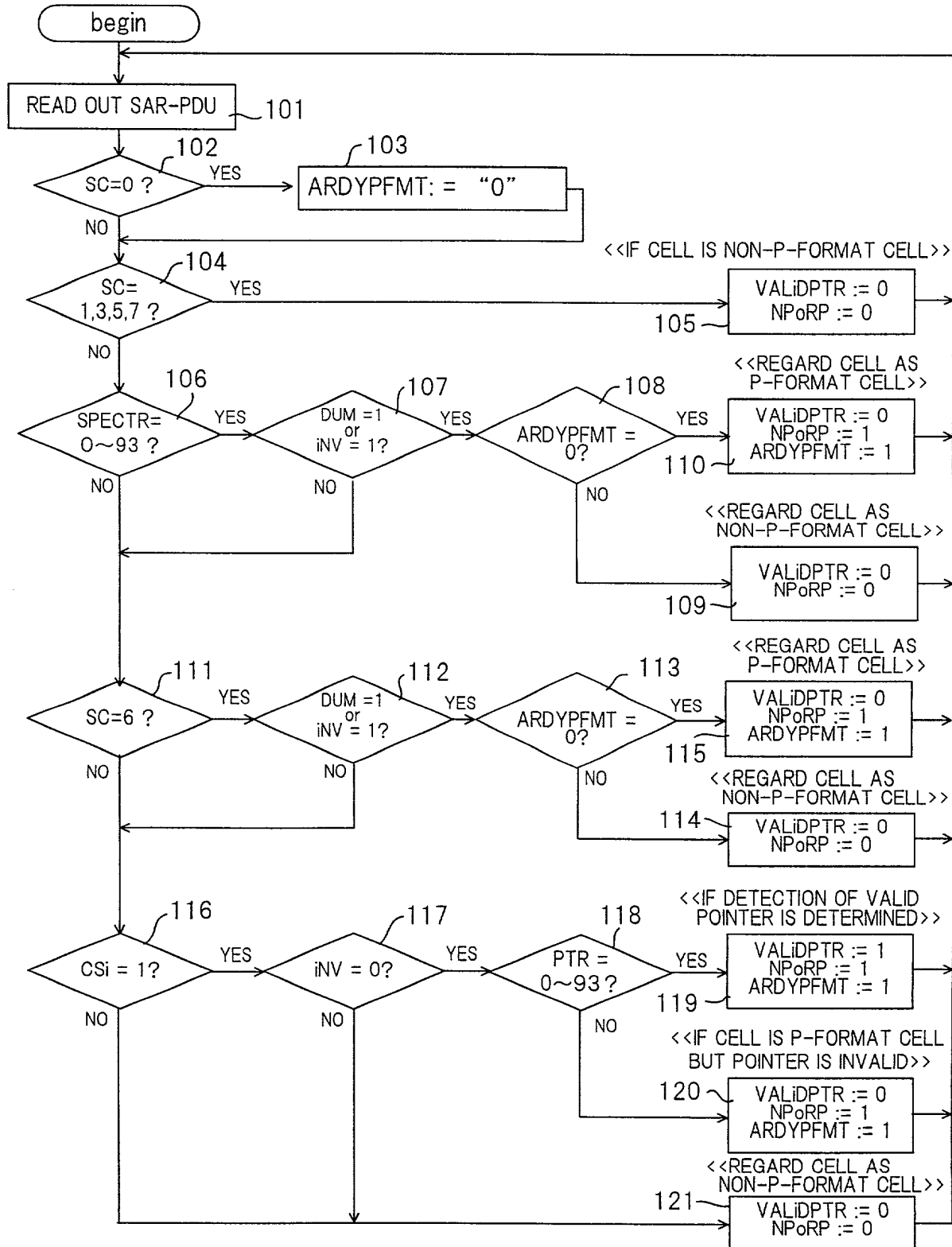


FIG. 25

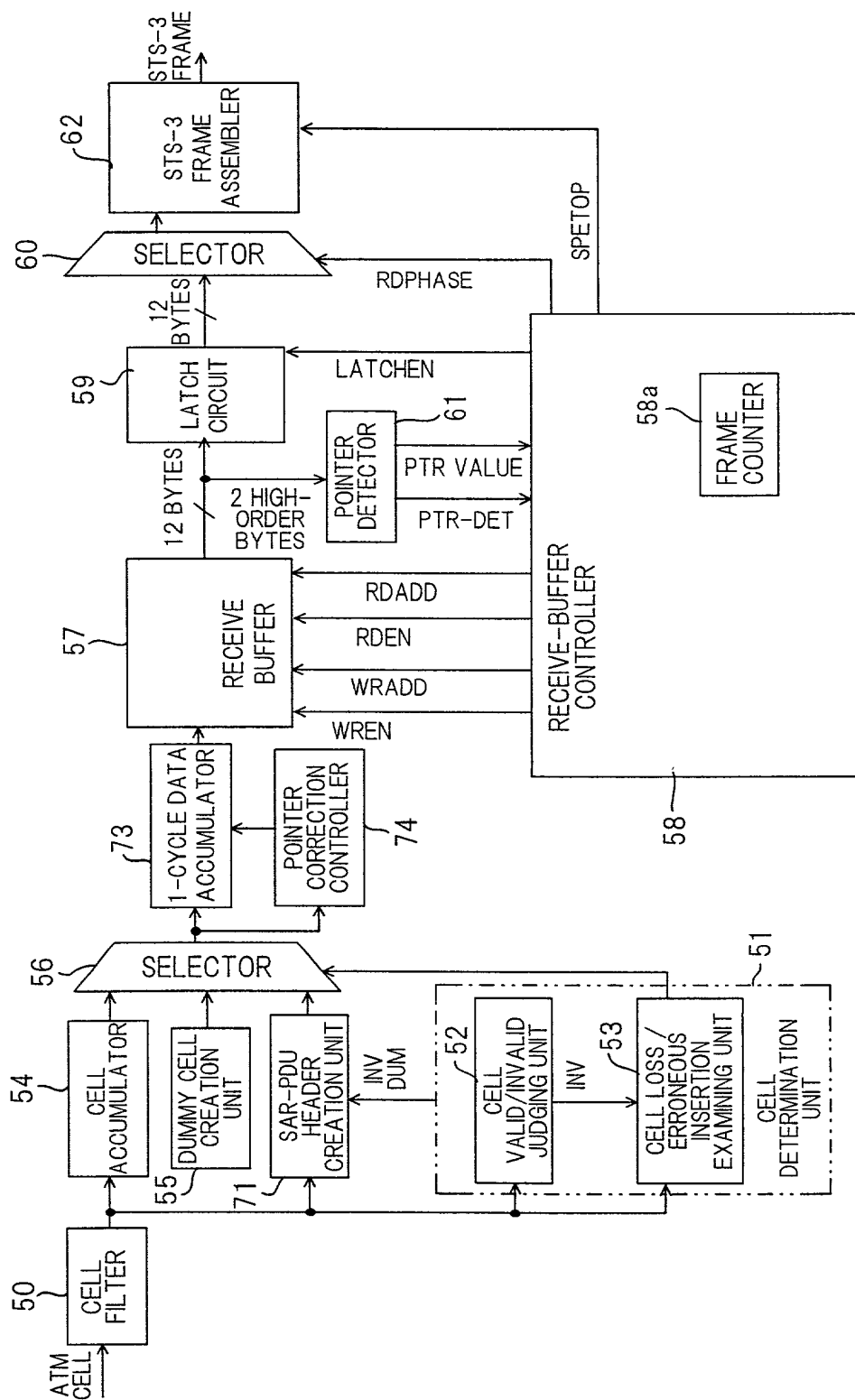


FIG. 26

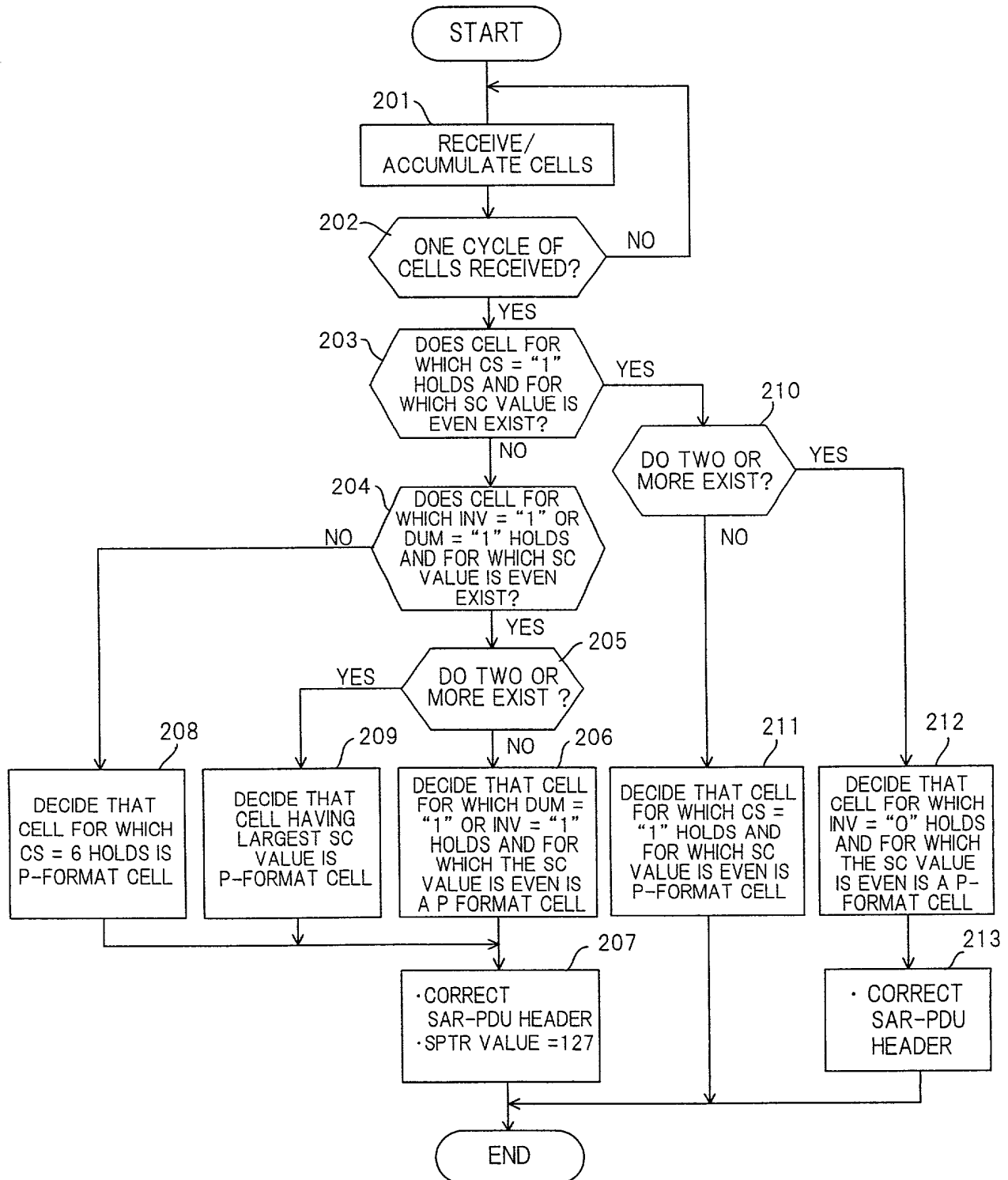


FIG. 27

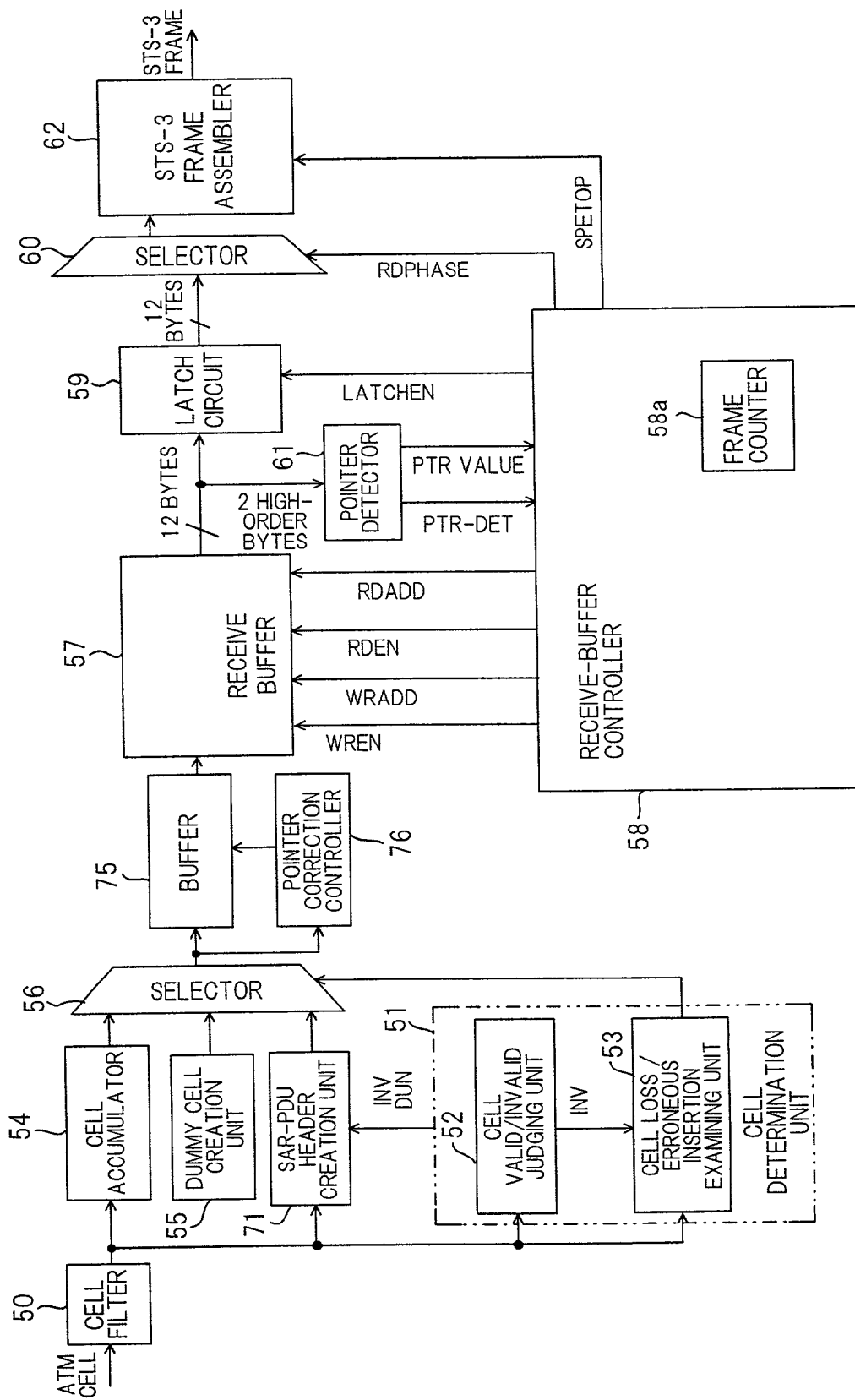


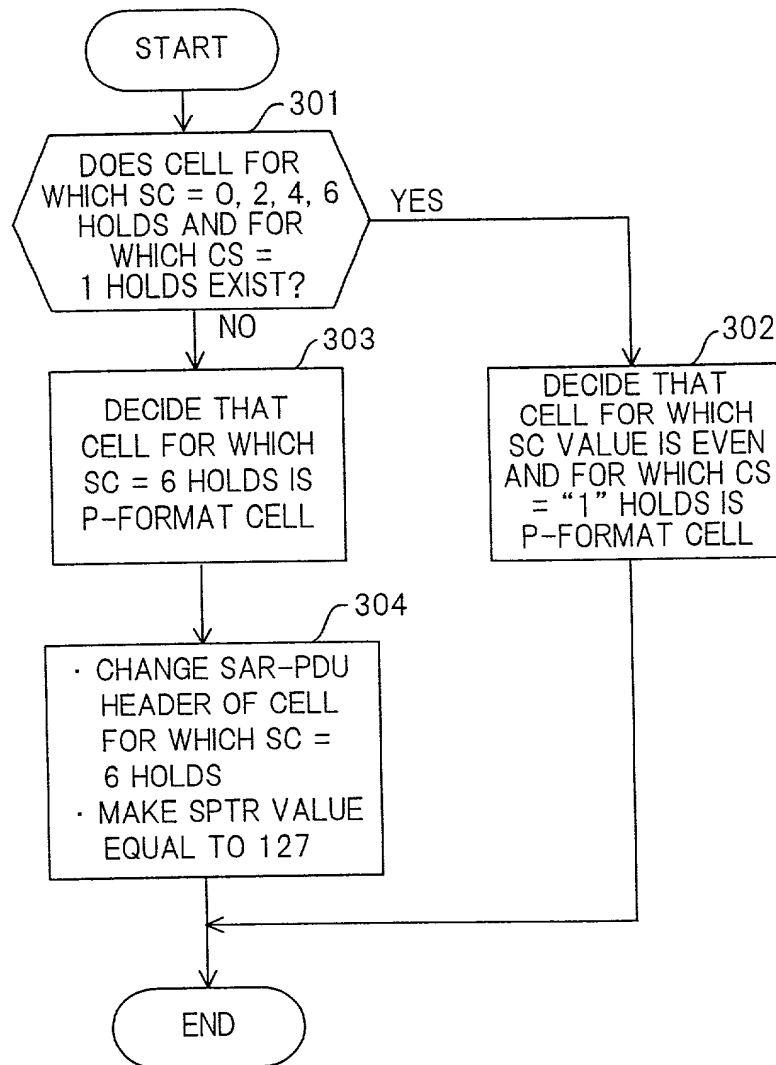
FIG. 28

FIG. 29

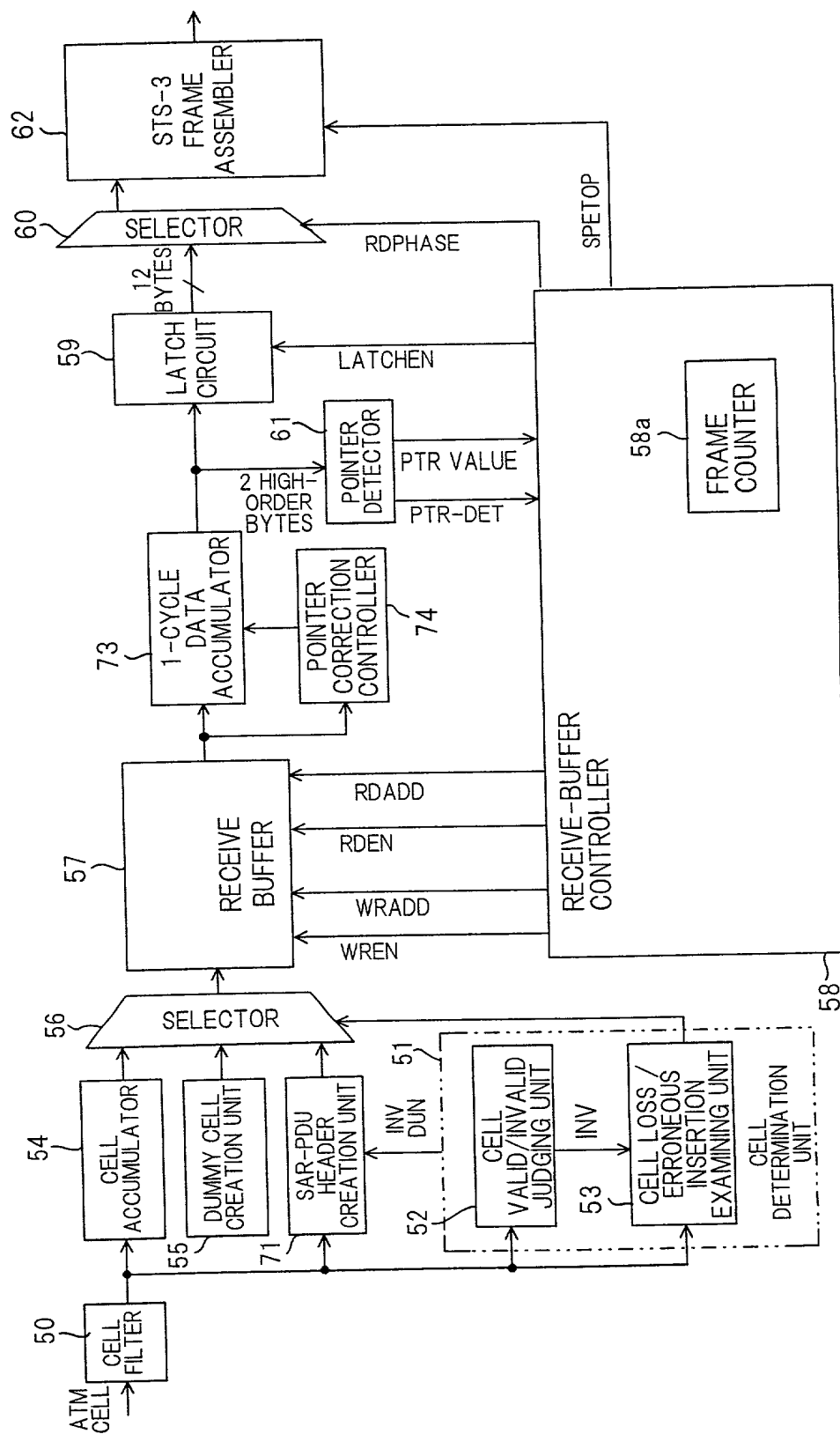


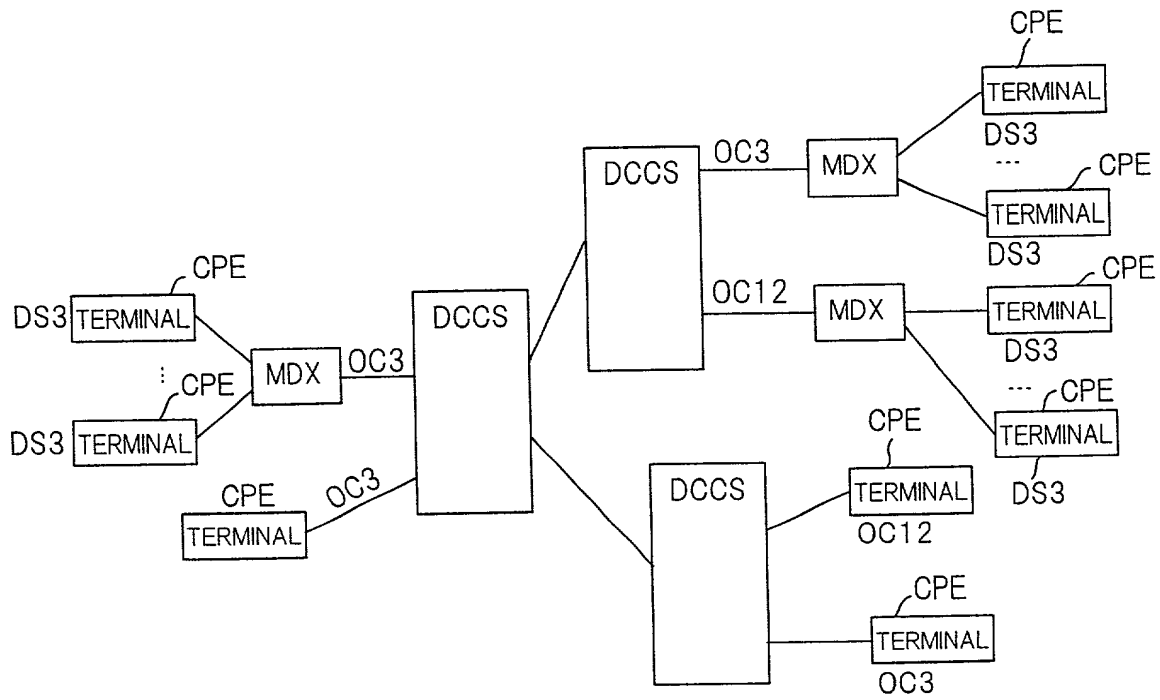
FIG. 30 PRIOR ART

FIG. 31 PRIOR ART